

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: K. TASAKI, et al.  
Application No.: 10/581,721  
Nat'l Stage Entry: JUNE 5, 2006  
For: MANUFACTURING METHOD FOR ELECTRONIC DEVICE  
Group AU: 2876  
Examiner: TBA  
Confirm. No.: 8570

**CORRECTED INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR 1.97 AND 1.98**

**Mail Stop: AMEND – NO FEE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

March 4, 2009

Sir:

Please find enclosed herewith a Corrected Information Disclosure Statement By Applicant (Form PTO/SB/08A), wherein the date of the Japanese Official Action (the second reference listed in the Non Patent Literature Documents section on the Form PTO/SB08A) for Application No. 2005-515960, dated October 24, 2008, has been corrected to reflect the correct date of the Japanese Official Action for Application No. 2005-515960 as October 21, 2008, not October 24, 2008. Please note that the correct date was recited in the Information Disclosure Statement itself.

This Information Disclosure Statement is being submitted prior to a first Office Action on the merits in the above-identified application, and in order to correct the record of the above-identified application. Accordingly, requirements of 37 CFR 1.97(b) are satisfied.

In view of the foregoing, it is respectfully submitted that all applicable requirements of 37 CFR 1.97 and 1.98 have been satisfied, in connection with the

March 4, 2009

documents listed on the enclosed Form. Accordingly, consideration of the listed documents, upon examination of the above-identified application, is respectfully requested.

Please charge any shortage of fees due in connection with the filing of this paper to the Deposit Account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (case No. 1204.46258X00), or credit any excess fees paid to such Deposit Account.

Respectfully submitted,

**ANTONELLI, TERRY, STOUT & KRAUS, LLP**

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